## POZNAN UNIVERSITY OF TECHNOLOGY



### EUROPEAN CREDIT TRANSFER AND ACCUMULATION SYSTEM (ECTS)

pl. M. Skłodowskiej-Curie 5, 60-965 Poznań

### **COURSE DESCRIPTION CARD - SYLLABUS**

Course name

Common-use electronics

**Course** 

Field of study Year/Semester

Electronics and telecommunications 1/2

Area of study (specialization) Profile of study

Multimedia and common-use electronics general academic
Level of study Course offered in

Second-cycle studies Polish

Form of study Requirements

full-time elective

**Number of hours** 

Lecture Laboratory classes Other (e.g. online)

30

Tutorials Projects/seminars

**Number of credit points** 

4

**Lecturers** 

Responsible for the course/lecturer: Responsible for the course/lecturer:

dr inż. Krzysztof Klimaszewski

krzysztof.klimaszewski@put.poznan.pl

#### **Prerequisites**

Extended knowledge in mathematics useful in formulating and solving problems in electronics and telecommunications arena.

Knowledge in the structure and architecture of programmable digital circuits and their practical use.

The developed knowledge, backed by mathematic background, about the basic circuit theory necessary for understanding, analysis and evaluation of operation of electric circuits.

The ability to freely communicate in English, especially about proffesional subjects, ability to use the trade literature in English. (books, technical and scientific journals, application notes, catalogs, manuals and standards and such)

The understanding of one's knowledge and abilities limitations, the necessity of constant training.

The awarness of the necessity of a proffesional approach to the technical problems to be solved and taking responsibility for the proposed technical solutions.

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# **Course objective**

The demonstration of typical circuit solutions applied in common-use circuits

Practical realisation of the design process of a chosen electronic circuit.

## **Course-related learning outcomes**

#### Knowledge

The structured knowledge of the properties and characteristics of electronic components, in the structure, analysis and circuit design aspects, including embedded systems, as well as designing of printed boards.

#### Skills

The ability to find the necessary information about modern integrated circuits and their applications in the designed circuits.

The ability to design and construct an analog or analog-digital electronic circuit.

The ability to design an electronic circuit using a microcontroller chosen specificly for the requirements of the project.

### Social competences

The knowledge of the limitations of one's knowledge and abilities, the necessity of further training.

The understanding of the importance of the safety of use of the electronic devices.

#### Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

Oral exam with approximately 3 questions concerning the subjects presented during the lectures.

The laboratory exercises are graded based on the result of the project - the designing and realization of the electronic circuit.

### **Programme content**

#### Lecture:

The areas of applications of electronic circuits

Supplying power to the electronic circuits - means, requirements and parameters

Safety of electronic circuits

The analysis of chosen designs of electronic circuits

Laboratories:

Measuring electronic circuit parameters

Designing of circuit boards

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Preparation of the manufacturing data

The assembly and debugging of the designed circuit

Measurements of the developed circuit

# **Teaching methods**

Lectures: multimedia presentation, illustrated by the examples shown on overhead projector, conversatory lecture

Laboratory exercises: brainstorming, group work

### **Bibliography**

Basic

"Sztuka elektroniki" P. Horowitz, W. Hill

#### Additional

"Projektowanie Układów Analogowych" R.A.Pease

# Breakdown of average student's workload

	Hours	ECTS
Total workload	100	4,0
Classes requiring direct contact with the teacher	75	3,0
Student's own work (literature studies, preparation for exam,	25	1,0
preparation of the project) <sup>1</sup>		

3

<sup>&</sup>quot;Analogowe Układy Elektroniczne" J.Boksa

<sup>&</sup>quot;The Art of Electronics: The x-Chapters" P. Horowitz, W. Hill

<sup>&</sup>lt;sup>1</sup> delete or add other activities as appropriate